Exploiting waveguide junctions for the switching of transverse electromagnetic pulses of non-constant amplitude

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Abstract

In this communication we will present how, by correctly engineering their amplitudes, transverse electromagnetic (TEM) square pulses traveling within multiple transmission lines interconnected in a series or parallel configuration can be exploited for high-speed switching and routing, enabling their exploitation in decision-making processes such as if...then...else. Two applications of this technique will be discussed during the conference, what we call a pulse comparator and director.

1. Introduction

Semiconductor switching elements have served as the backbone of computing for many decades. Specifically, the MOS-FET (metal-oxide field effect transistor) is a remarkable switching device which, when connecting several of them, can be used to produce more complex switching operations such as the well-known OR, NOR and XOR logics gates. These devices are widely used in commercial computers, but as the demand for faster computing systems grows so does the complexity of the computing devices used as the fundamental switching elements. In this realm, large arrays of semiconductor switching elements may become a challenge in terms of energy consumption and speed in dynamic switching, as parasitic capacitances formed between the devices and the substrates they reside on consume energy and time as they charge and discharge\(^{(1)}\)–\(^{(3)}\).

In recent years there has been a great deal of research effort invested into new computing paradigms. New technologies have been reported such as quantum computing\(^{(4)}\), biological computing\(^{(5)}\), \(^{(6)}\), and computing with optical soliton collisions\(^{(7)}\), \(^{(8)}\). Computing with electromagnetic (EM) waves is a new research area which is rapidly expanding due to its potential to provide new and high-speed computing processes. In this realm metamaterials and metasurfaces (their 2D equivalent) have seen significant application due to their ability to provide spatiotemporal modulation of the materials properties \(^{(9)}\)–\(^{(13)}\). Metamaterials-based devices have been recently proposed to perform mathematical operations such as differentiation, integration and convolution\(^{(14)}\)–\(^{(16)}\) as well as to solve ordinary differential equations and integral equations \(^{(17)}\)–\(^{(19)}\).

In this work we will present our most recent finding in exploiting transverse electromagnetic (TEM) pulse interactions in waveguide networks for high-speed decision-making processes\(^{(20)}\). To demonstrate our technique, we present two examples of decision-making processes made possible by correctly engineering the amplitudes of incident TEM pulses. We call the first of these examples a comparator, which is capable of switching between two output states based on which is the larger of two inputs and the second a director, which utilizes the intrinsic reciprocity of the waveguide junctions to perform decision-making processes.

2. Results and discussion

2.1 TEM pulse splitting at waveguide junctions

Our proposed method takes advantage of the splitting and superposition of TEM pulses at waveguide junctions. These waveguide junctions can be described by a characteristic scattering matrix which determines how an input TEM pulse from each of the ports is routed, with a portion of the energy being transmitted to the other ports of the junction and a portion of the energy being reflected back to the incident port. When all waveguides are identical, in cross-section and filling material (i.e. same characteristic impedance\(^{(21)}\)) this scattering matrix can be written as \(A = I - \gamma J\) or \(A = I + \gamma J\) for parallel and series junctions respectively\(^{(22)}\), \(^{(23)}\). Here \(I\) and \(J\) are the identity and all ones matrices of size \(N \times N\) respectively, where \(N\) is the number of ports at the waveguide junction, and \(\gamma = 2/N\) is the junctions transmission coefficient. Equation (1) shows the scattering matrix of a three-port parallel junction as an example.

\[
\begin{pmatrix}
\frac{1}{3} & \frac{2}{3} & \frac{2}{3} \\
\frac{2}{3} & \frac{1}{3} & \frac{2}{3} \\
\frac{2}{3} & \frac{3}{3} & \frac{2}{3}
\end{pmatrix}
\begin{pmatrix}
P_1 \text{ input} \\
P_2 \text{ input} \\
P_3 \text{ input}
\end{pmatrix}
= 
\begin{pmatrix}
P_1 \text{ output} \\
P_2 \text{ output} \\
P_3 \text{ output}
\end{pmatrix}
\]

Equation (1) shows the scattering matrix of a three-port parallel junction as an example.

During the conference, it will be discussed in detail how by engineering the amplitudes of the incident pulses decision-
making processes (such as if... then... else) can be performed.

2.2 TEM pulse comparator

As an example of a decision-making process, we present the operation of a “pulse comparator”. The purpose of this device is to switch between two output states, given by the polarity of the pulse observed at the output port ($P_3$ in Fig.1 a-c), depending on which is the larger of two quantities $\phi_1$ or $\phi_2$. This is done by exciting a TEM pulse of amplitude $\phi_1$ at $P_1$ and another of amplitude $-\phi_2$ at $P_2$. The pulse observed at $P_3$ has positive polarity if $\phi_1 > \phi_2$ and negative polarity when $\phi_1 < \phi_2$, thus demonstrating a simple if... then... else operation. A schematic representation and simulation of this can be seen in Fig.1.

![Figure 1. TEM pulse comparator. a and b, show the cases where $\phi_1 > \phi_2$ and $\phi_1 < \phi_2$ respectively. Here the larger of the two values is set to 2V and the smaller to 1V. The polarity of the output at $P_3$ switches between the two cases, being positive in a and negative in b. c, Full-wave simulations results of the case shown in b. Simulation was performed using the commercially available software CST Studio Suite® showing an excellent agreement with the theoretical values.](image)

2.3 TEM pulse director

As another example of a decision-making process, we will present a structure that we call a pulse director. The aim of this structure is to redirect all the incident pulse energy towards a single waveguide port. To do this a “control” pulse is required incident from the port we wish to direct the pulses towards (as can be seen at $P_4$ in Fig.2). For a given “control” pulse from $P_4$ there exists only one combination of input signals which will result in all energy being directed towards $P_4$ after the junction interaction has taken place, we refer to this combination as matched inputs.

The switching between output states when the pulse director is excited with a matched input combination and an unmatched input combination can be seen in Fig.2. In the matched case no reflection towards $P_3$-$P_4$ is observed, while the opposite is true for an unmatched case. Logical switching can be performed by considering any inputs which are a part of this matching condition as logical “high” and inputs which are not as logical “low”. If any of the inputs do not correspond to the value required for a matched input combination (i.e. if not all inputs are high) then a portion of the energy in the system will be redirected towards the input ports, thus enabling AND functionality.

![Figure 2. TEM pulse director schematic. a and b show the cases where the incident pulse amplitudes are matched and unmatched, respectively, demonstrating how there are and there are no reflections present in the system, respectively. The left pannel of a and b also represent a potential three input AND gate with the inputs from $P_1$ – $P_3$ being labeled with logical “1” or “0” respectively. a represents the high (“1”) output case while b represents a potential low (“0”) output case.](image)

3. Conclusions

We have presented a computing technique that exploits the splitting of TEM pulses of differing amplitudes being excited from multiple ports of a $N$ waveguide junction in both series and parallel configurations. This technique was exploited for high-speed decision-making processes, with two examples of such processes being presented. This technology and technique may find applications in TEM pulse-based processor and could potentially be merged with existing electronic systems.
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