

A Compact Differential Coupler Using Substrate Integrated Suspended Slot Line (SISSL)

Yongqiang Wang* and Kaixue Ma

School of Microelectronics, Tianjin University, Tianjin 300072, China
wangyongqiang008@gmail.com; makaixue@tju.edu.cn

Abstract

In this paper, a differential coupler using substrate integrated suspended slot line (SISSL) is proposed. It uses a strip-slot hybrid coupler to obtain compact size. A high level of common-mode suppression is achieved. The proposed SISSL differential coupler has advantages of compact size, self-packaging, and low loss.

1 Introduction

Directional coupler is a widely used passive component in the RF and millimeter-wave circuit and system. Recently, differential or balanced couplers have received much attention since they can better suppress the environmental noise and reduce the crosstalk and electromagnetic interference [1-4]. Slot line [5] is a differential/balanced transmission line with the inherent ability to suppress common-mode noise, and has been widely used to design differential filters [6] and dividers [7]. But the slot line-based circuit has radiation loss, and it usually requires a metal box for packaging.

In [8], the slot line is embedded and suspended in the air cavity of the substrate integrated suspended line (SISL) platform [9], and this new form of slot line with shielding boundary is named as substrate integrated suspended slot line (SISSL). SISSL has advantages of low loss and self-packaging as compared with the slot line with open form. Two differential branch-line couplers are designed in [8], but the circuit size is a little large.

In this paper, a compact differential coupler based on SISSL platform is proposed. It uses a strip-slot hybrid coupler and has a compact size. The SISSL differential coupler has a wide operational bandwidth for the differential-mode, and good common-mode suppression is also obtained.

2 Circuit Design

Figure 1 shows the three-dimensional view of the proposed compact SISSL differential coupler. It has five boards named as board1 to board5, and ten metal layers from M1 to M10. By using via holes and metal layers, the multi-layer circuit is self-packaged. The main circuit is designed on M5 and M6, and it is EM-shielded.

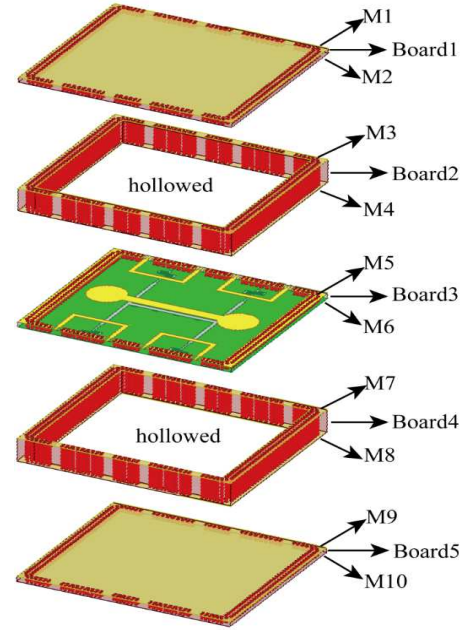


Figure 1. Three-dimensional view of the proposed compact SISSL differential coupler.

Figure 2 shows the planar view of the differential coupler. It has eight single-ended ports, forming four pairs of differential ports. The SISSL is designed on M6 and the shielded microstrip is designed on M5. The inner core circuit is a strip-slot coupler [10], which is composed of a strip on M5 and slot line on M6. This type of hybrid coupler features a compact size as compared to the traditional branch-line coupler. At the ports of the strip-slot coupler, transitions from SISSL to differential shielded microstrip are used [8]. The common-mode suppression is obtained by using the inherent nature of the operation of slot-line.

For the strip-slot coupler, the length of the coupling region is about a quarter guided wavelength. It is noted that the slot line in the coupling region is meandered as seen in Figure 2 (c), and it has a longer physical length than that of the strip line. This is mainly because the dispersion characteristics of strip line and slot line are different [10]. The board material in this paper is same as that in [8], and the center frequency is chosen as 7 GHz. The design method can refer to [10] and [8]. The detailed dimension values are provided in Figure 2 (b) and (c).

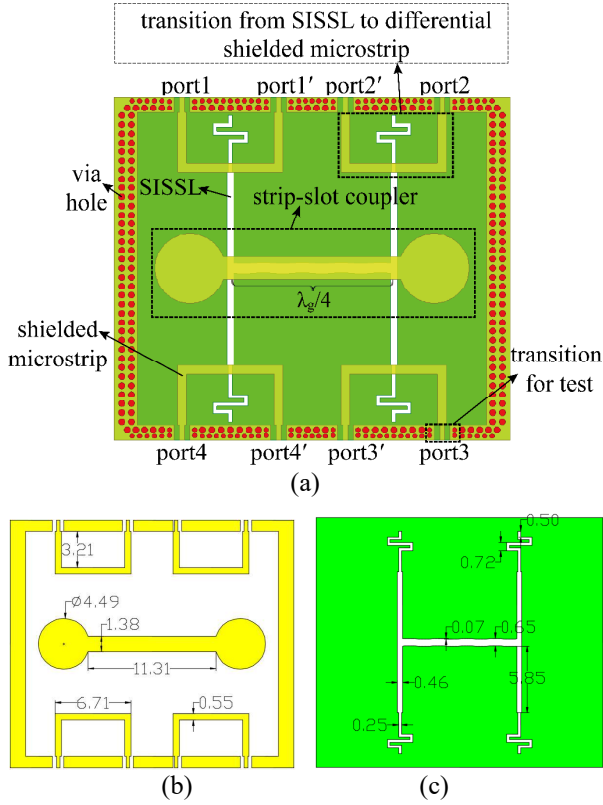


Figure 2. (a) Planar view of board3. (b) M5. (c) M6. units:mm

3 Results

The simulated response is shown in Figure 3. From 6.36 GHz to 7.66 GHz, the simulated return loss and isolation for the differential mode are both better than 20 dB, and the phase difference between two output ports is $90^\circ \pm 0.7^\circ$. The simulated insertion loss for the differential-mode is 0.2 dB at 7 GHz. From 6 GHz to 8 GHz, the common-mode suppression and cross-mode rejection are almost larger than 30 dB. The core circuit area is $19.87\text{mm} \times 22.3\text{mm}$.

4 Conclusions

This paper presents a compact differential coupler based on SISSL platform. It not only has good differential-mode response, but also features wideband common-mode suppression. Good properties in terms of compact size and self-packaging are obtained.

5 Acknowledgements

This work was supported in part by the National Natural Science Foundation of China for Key Project under Grant 61831017, and in part by the National Natural Science Foundation of China for Distinguished Young Scholar under Grant 61625105.

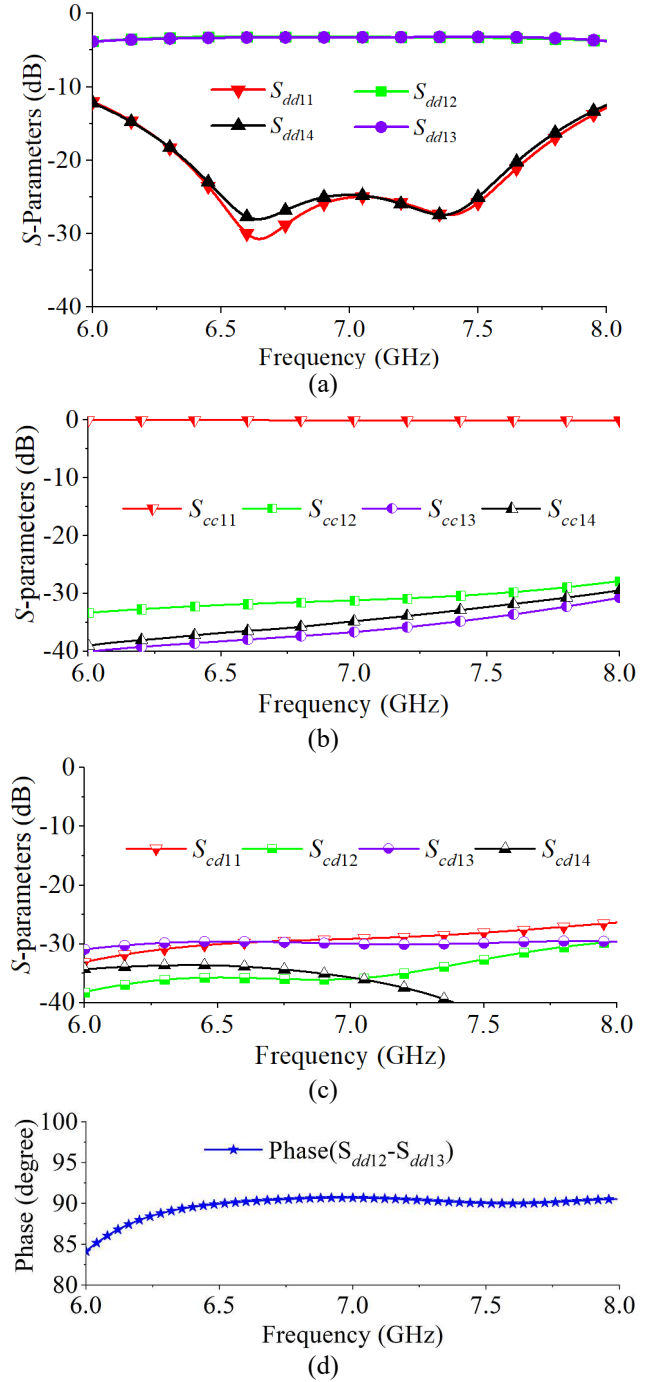


Figure 3. Simulated response of the SISSL differential coupler. (a) Differential-mode. (b) Common-mode. (c) Cross-mode. (d) phase for the differential-mode.

6 References

1. J. Martel, A. Fernández-Prieto, J. L. M. del Río, F. Martín, and F. Medina, "Design of a Differential Coupled-Line Directional Coupler Using a Double-Side Coplanar Waveguide Structure With Common-Signal Suppression," *IEEE Trans. Microw. Theory Techn.*, **69**, 2, pp. 1273–1281, Feb. 2021, doi: 10.1109/TMTT.2020.3041226.

2. W. Feng et al., "Balanced Rat-Race Couplers With Wideband Common-Mode Suppression," *IEEE Trans. Microw. Theory Techn.*, **67**, 12, pp. 4724–4732, Dec. 2019, doi: 10.1109/TMTT.2019.2946158.
3. J. Shi, J. Qiang, K. Xu, and J.-X. Chen, "A Balanced Branch-Line Coupler With Arbitrary Power Division Ratio," *IEEE Trans. Microw. Theory Techn.*, **65**, 1, pp. 78–85, Jan. 2017, doi: 10.1109/TMTT.2016.2613052.
4. Y. Wang, K. Ma, and S. Mou, "A Transformer-Based 3-dB Differential Coupler," *IEEE Trans. Circuits Syst. Regul. Pap.*, **65**, 7, pp. 2151–2160, Jul. 2018, doi: 10.1109/TCSI.2017.2778000.
5. S. B. Cohn, "Slot Line on a Dielectric Substrate," *IEEE Trans. Microw. Theory Techn.*, **17**, no. 10, pp. 768–778, Oct. 1969, doi: 10.1109/TMTT.1969.1127058.
6. L. Yang, L. Zhu, W.-W. Choi, K.-W. Tam, R. Zhang, and J. Wang, "Wideband Balanced-to-Unbalanced Bandpass Filters Synthetically Designed With Chebyshev Filtering Response," *IEEE Trans. Microw. Theory Techn.*, **66**, 10, pp. 4528–4539, Oct. 2018, doi: 10.1109/TMTT.2018.2860949.
7. H. Zhu, J.-Y. Lin, and Y. J. Guo, "Filtering Balanced-to-Single-Ended Power Dividers With Wide Range and High Level of Common-Mode Suppression," *IEEE Trans. Microw. Theory Techn.*, **67**, 12, pp. 5038–5048, Dec. 2019, doi: 10.1109/TMTT.2019.2944383.
8. Y. Wang, M. Yu, and K. Ma, "Substrate Integrated Suspended Slot Line and Its Application to Differential Coupler," *IEEE Trans. Microw. Theory Techn.*, **68**, 12, pp. 5178–5189, Dec. 2020, doi: 10.1109/TMTT.2020.3026012.
9. K. Ma and K. T. Chan, "Quasi-planar circuits with air cavities", PCT Patent WO2007149046, 2007
10. B. Schiek, "Hybrid Branchline Couplers - A Useful New Class of Directional Couplers," *IEEE Trans. Microw. Theory Techn.*, **22**, 10, pp. 864–869, Oct. 1974, doi: 10.1109/TMTT.1974.1128365.