FULL-WAVE SIMULATION OF TRANSMISSION LINE STRUCTURES IN MICROWAVE MONOLITHIC INTEGRATED CIRCUITS

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ABSTRACT

Future Si-based analog and digital highly integrated Microwave Monolithic Integrated Circuits (MMIC) are expected to operate at frequencies up into the millimeter-wave range. Further reduction of circuit dimensions will require multilayered wiring and transmission line structures with cross-sectional dimensions of the conductors in the order of micrometers. In order to achieve further reduction in conductor losses copper is also considered for internal wiring. The exact modelling of the transmission properties of the line structures requires full-wave modelling of the electromagnetic field in the semi-conductor and insulator regions as well as within the conductors. In this paper various transmission line structures have been analyzed using the Mode Matching (MM) approach. Based on the numerical simulation transmission an equivalent network-oriented circuits are extracted.

INTRODUCTION

The application of copper as conductor material and the use of damascene technology is a promising way for future realization of highly integrated MMICs for frequencies of operation up to several 10 GHz. Compared to aluminium copper exhibits significantly lower electro-migration and better electrical and thermal conductivity [1, 2]. The higher possible current density in copper transmission lines allows a decrease in the cross-section of the conductors. Damascene technology allows one to reduce the size of transmission lines to 2.5 µm width and 0.4 µm height. The dimensions of the conductors are comparable with the skin depth, therefore an accurate simulation and optimization of the structures of the transmission lines requires full-wave modelling of the electromagnetic field inside the conductors as well.

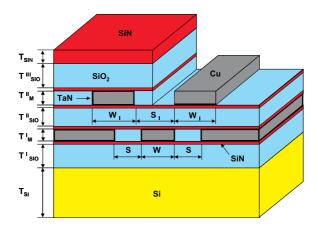


Figure 1: Schematic view of the simulated multilayered geometry.

TECHNOLOGY

In damascene technology trenches for the transmission lines and vias are first etched into the substrate (SiO₂). A very thin insulating layer of TiN or TaN is then deposited as a barrier between the substrate and copper to avoid diffusion of the copper into the SiO_2 layer. A thin copper film is evaporated and all trenches and holes for vias are filled at once in a galvanic process (dual damascene process). The surplus copper on the surface of the wafer is removed afterwards by chemical-mechanical polishing, then the surface is covered by etching stopper (Si_3N_4). After this a further SiO_2 layer can be placed on top and these steps may be repeated up to 6 times. The result is a multilayered structure with various transmission line structures, i.e. coplanar waveguide (CPW), stripline, microstrip line, conductor backed CPW (see Fig. 1).

THE MODE MATCHING APPROACH

In transmission lines with strip width and height in the skin depth's order of magnitude conductor loss influences the propagation characteristics severely. One of the most efficient methods for numerical simulation of such homogeneous two-dimensional structures is the mode matching method [4, 3]. The field inside the conductors, playing an important role, is completely taken into consideration. In the MM method the structure is subdivided into homogeneous subdomains. At the boundaries of the subdomains the tangential field components of the partial waves are matched. The method of moments is used to determinate the amplitudes of the partial waves. Truncating the complete set of eigenmodes numerical results depend on the set of testing functions used in the method of moments.

SIMULATIONS RESULTS

Various types of transmission lines are considered. From various investigations it turned out that the coplanar waveguides are advantageous compared to other line types in respect of the attenuation caused mainly by the conductor loss. For example, microstrip lines always show higher attenuation due to the thickness of the SiO₂ layer which is fixed by technological reasons. Two 50 Ω CPWs of different dimensions are simulated. The simulations are performed with following material parameters: $\varepsilon_{\rm Si} = 11.9$, $\sigma_{\rm Si} = 5.5 \cdot 10^{-4} \, \text{s/m}$, $\varepsilon_{\rm SiO_2} = 3.9$, $\tan \delta_{\rm SiO_2} = 10^{-4}$, $\sigma_{\rm Cu} = 5.9 \cdot 10^7 \, \text{s/m}$, vertical dimensions: $T_{\rm Si} = 380 \, \mu \text{m}$, $T_{\rm SiO_2}^I = 4.9 \, \mu \text{m}$, $T_{\rm M}^I = 0.6 \, \mu \text{m}$, $T_{\rm SiO_2}^{II} = 4.15 \, \mu \text{m}$, $T_{\rm SiN} = 0.55 \, \mu \text{m}$. The first CPW (CPW1) has the dimensions: $W = 7.5 \, \mu \text{m}$, $S = 2.5 \, \mu \text{m}$; the size of the second CPW (CPW2) is twice as large: $W = 15 \, \mu \text{m}$, $S = 5 \, \mu \text{m}$. In Fig. 2 the numerical results for the characteristic impedance of two CPWs are shown in dependance of the number n

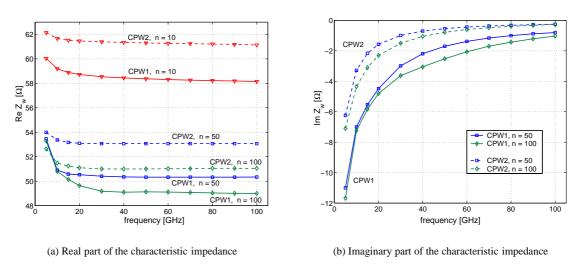


Figure 2: Characteristic impedance of different CPWs.

of partial waves considered in the simulation. Due to the losses, the imaginary part of the wave impedance (see Fig. 2(b)) cannot be neglected. In Fig. 3(a) the attenuation coefficient in $^{dB/mm}$ of different CPW geometries is compared. The attenuation coefficient increases severely with frequency, especially for CPW1. The substrate attenuation is lower than $0.005\,^{dB/mm}$. In order to obtain accurate numerical solutions at higher frequencies the number of partial waves should be significantly increased. The effective permittivity is presented in Fig. 3(b) for the same structures. The electromagnetic field concentrates more in the SiO_2 substrate with increasing frequencies. On the basis of numerical results useful network-oriented models can be obtained through equivalent circuit representation. The characteristic impedance of a transmission line may be represented by

$$Z_{w} = \sqrt{\frac{Z'(\omega)}{Y'(\omega)}} = \sqrt{\frac{R' + j\omega L'}{G' + j\omega C'}}.$$
 (1)

The loss in the conductor is described by the series resistance per unit length R' which is at the same time frequency dependent due to the skin effect and increases proportionally with the square root of the frequency. The inductance per unit length L' represents a component dependent on the geometry and the skin effect. The conductance per unit length G' portrays the loss associated with the transverse electric field in Si/SiO_2 substrates while C' represents the capacitance per unit length. The impedance $Z'(\omega)$ describing the skin effect in conductor can be modelled with arbitrary accuracy by

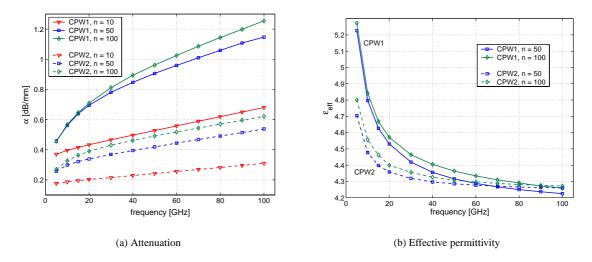


Figure 3: Attenuation and effective permittivity of different CPWs.

ladder network [5] according to Fig. 4(a). The complex impedance $Z'(\omega)$ with a finite number of LR-sections is given recursively by

$$Z'(\omega) = \left\{ \begin{array}{c} j\omega L'_1 + \frac{1}{\frac{1}{R'_1} + \frac{1}{j\omega L'_2 + \frac{1}{\frac{1}{1-k}}}} \\ & \ddots \\ & & + \frac{1}{\frac{1}{R'_{m-1}} + \frac{1}{j\omega L'_m + R'_m}} \end{array} \right\}. \tag{2}$$

The *LR*-ladder networks represents a wide-band model of the skin effect. The accuracy of the model increases with the number m of *LR*-sections. Fig. 5(a) and Fig. 5(b) compares the frequency dependencies of the real and the imaginary part of $Z'(\omega)$ at m = 10. The admittance $Y'(\omega)$ is modelled by a *CG*-ladder network as depicted in Fig. 4(b). Fig. 6(a) and Fig. 6(b) show the approximation of $Y'(\omega)$ at k = 3.

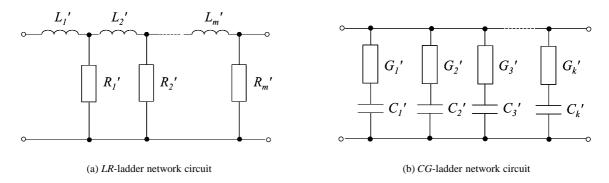


Figure 4: Network circuits.

CONCLUSION

In this contribution the full-wave analysis and comparative study of different transmission lines using the MM technique are presented. High-frequency and millimeter-wave MMICs in damascene technology are made up of transmission line structures with cross section in the micrometer range. This requires full-wave modelling of the electromagnetic waves propagating on the lines. Based on the results an equivalent circuit for the transmission line is extracted, which can be used in different network-oriented circuit simulators.

ACKNOWLEDGEMENT

The authors would like to thank the Bundesministerium für Bildung und Forschung for financial support, and their project partners from Infineon, Daimler Chrysler and Technische Universität Chemnitz for stimulating discussions.

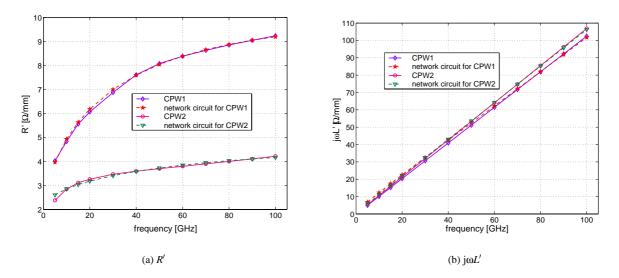


Figure 5: Real and imaginary part of $Z'(\omega)$ impedance.

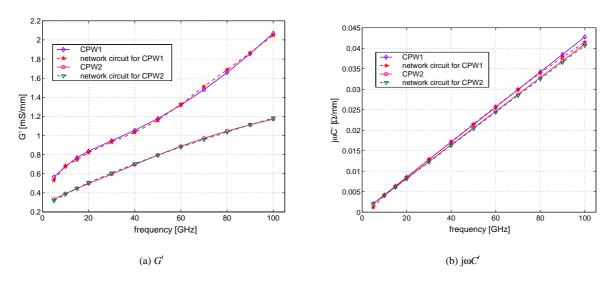


Figure 6: Real and imaginary part of $Y'(\omega)$ impedance.

REFERENCES

- [1] K. Weiss, S. Riedel, S. Schulz, H. Heldener, M. Schwerd and T. Gessner, Characterization of Electroplated and MOCVD Cooper for Trench Fill in Damascene Architecture, *Proc. of the Materials for Advanced Metalization Conference*, pp. 125–132 Orlando, FL, Sep. 1999.
- [2] H. Heldener, H. Körner, A. Mitchell, M. Schwerd and U. Seidel, Comparison of Copper Damascene and Aluminum RIE Metallization in BiCMOS Technology, *Proc. of the Materials for Advanced Metallization Conference*, Stresa, Italy, Mar. 2000.
- [3] J. Kessler, R. Dill and P. Russer, Field theory investigation of hight-Tc superconducting coplanar waveguide transmission lines and resonators, *IEEE Trans. MTT*, vol.39, pp.1566–1574, Sept. 1991.
- [4] R. Schmidt and P. Russer, Modeling of Cascaded Coplanar Waveguide Discontinuities by the Mode-Matching Approach, *IEEE Trans. MTT*, vol.MTT-43, no.12, pp.2910–2917, Dec. 1995.
- [5] P. Russer and B. Isele, Modelling of skin effect in TLM, *Intl. Conf. Ultra-Wideband, Short-Pulse Electromagnetics*, New York, NY, Oct. 1992.